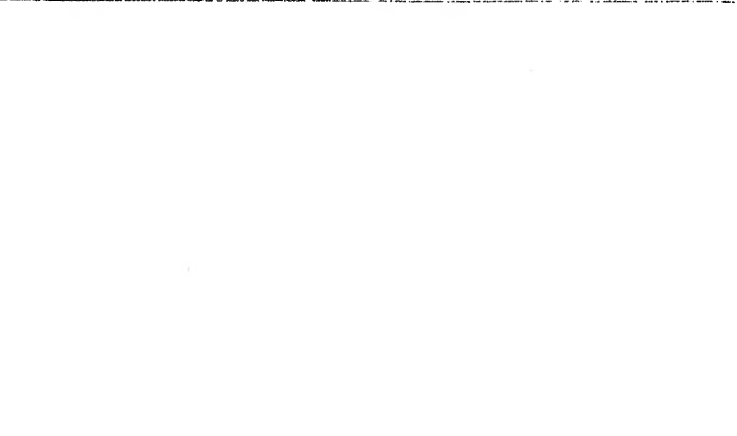


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1	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6892281 B2	20050510	11	Apparatus, method, and system for reducing latency of	711/137	711/154; 711/168;		Chaudhari; Sunil B. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
2	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6889295 B2	20050503	11	Re-ordering requests for shared resources	711/151	711/150; 711/158;		Watts; Jonathan Manuel	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
3	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6559850 B1	20030506	18	Method and system for improved memory access in	345/520	345/531; 710/306;		Strongin; Geoffrey S. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
4	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 20040111567 A1	20040610	17	SIMD processor with multi-port memory unit	711/149	711/168; 711/169		Anderson, Adrian John et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
5	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6272600 B1	20010807	21	Memory request reordering in a data processing system	711/140	711/133; 711/151;		Talbot; Gerry R. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
6	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6182192 B1	20010130	15	Memory interface device and method for accessing memories	711/111	365/191; 711/5		Rovati; Fabrizio	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
7	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 20040024948 A1	20040205	12	Response reordering mechanism	710/311			Winkler, Joerg et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US

710/ 6,54, 810

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310-311

711/137-151